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Dual Grid Voltage Modulated Direct Power Control of Grid-Connected Voltage Source Converter under Unbalanced Network Condition

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Abstract—The Grid voltage Modulated Direct Power Control (GVM-DPC) for Voltage Source Converter (VSC) is a recently proposed superior control strategies which features several advantages such as fast power reference tracking, simple structure, no need to use synchronizing techniques, strong robustness and so on. In this paper, an improved Dual Grid Voltage Modulated Direct Power Control (D-GVM-DPC) strategies for unbalanced grid voltage condition is proposed and compared with regular voltage oriented dual PI control techniques. The idea of this improvement is to separately control the positive and negative sequence power components based on the original GVM-DPC controller. The simulation results shows that the proposed strategy is superior to the dual current control schemes been used with voltage oriented control strategy.

Index Terms—Unbalance condition, Voltage Source Converters, Direct Power Control (DPC)

I. INTRODUCTION

Voltage source converters (VSC) are widely used in modern renewable distributed power systems nowadays. As the newly constructed renewable energy sources are generally located in remote areas, the grid-connected VSCs must capable to deal with severe grid conditions, which put forward a high requirements for its performance [1].

Current control strategies of converters can be mainly classified into two categories: Voltage-Oriented Control (VOC) and Direct Power Control (DPC). In VOC strategies, the controller decomposes the currents into two components which represent the active and reactive powers and uses Proportional-Integral (PI) controllers to regulating them separately. The concept of Direct Power Control (DPC) strategy has been proposed for years, which features several advantages, the most prominent point is that it has a fast convergence rate when tracking the power reference values compare to VOC. Besides, the phase lock loop is no longer necessary for DPC strategy, therefore the controller is simpler than the classical VOC [2]–[6]. However, traditional Look-Up Table (LUT) based DPC structure has a

obvious drawback. Because it uses hysteresis comparators to generate control signals, the switching frequency of controller is variable according to the state of operation, which will cause the harmonic spectrum of powers under steady state seriously [4].

G.Abad proposed a LUT-based predictive DPC technique for VSCs in doubly fed induction machine to resolve the problem [7]. Other solutions are mainly focus on modifying the generation of DPC control signal by using traditional Pulse Width Modulation (PWM). This type of solutions are widely proposed and discussed recently in many papers. [8] proposes an improved DPC for three-phase PWM rectifier with simple calculation. A nonlinear sliding-mode based DPC is proposed in [3]. backstepping control has been proposed for the DPC to improve its performance [9]. A dead-beat predictive strategy DPC for VSC is proposed and validated in [10].

Recently, a newly proposed DPC control strategy called Grid voltage modulated DPC (GVM-DPC) is proposed in [2], [4], [11]. The structure has several advantages such as fast dynamic response, simple structure and high robustness. The key point of the strategy is to transform the nonlinear system into a linear time-invariant system which simplifies the analysis of controlling unit considerably.

The improved DPC based strategies for unbalance grid condition are also discussed in many recent papers. [6] proposes an improved sliding mode control based DPC, by adding compensation terms into control signals, the ripples of output currents and powers can be restrained separately. Relationship between 2 different DPC methods for PWM rectifiers under unbalance grid condition are discussed in [12].

However, GVM-DPC is a newly proposed control strategy thus its improvement for unbalanced grid condition is still limited. A improvement of GVM-DPC for unbalanced grid condition is proposed in [5], which select three power compensation objectives to cancel the negative sequence of output currents. This is a common practice to improve the performance of DPC under unbalanced voltage condition. In this paper, a newly designed solution for GVM-DPC to eliminate negative

sequence components of currents is proposed. The main idea is applying dual power control schemes to GVM-DPC, which is similar to dual current control schemes been used with general VOC strategies. The positive and negative power sequences are separately calculated and controlled by two different controllers. The positive power sequence controller is designed to tracking the reference values of powers, and the negative power sequence controller is designed to eliminate the negative sequence of currents. The simulation results show that the proposed method can eliminate negative sequence components of currents under unbalanced grid conditions effectively.

The rest of paper is organized as follows, the mathematical modeling of converter under unbalanced condition is discussed in section II. In the section III, the design of proposed D-GVM-DPC is discussed. The simulation results are presented in the third section IV.

II. MATHEMATICAL MODELING OF AC-SIDE UNDER UNBALANCED GRID CONDITIONS

Consider a grid-connected voltage source inverter in stationary reference frame and take the inflow power direction as positive. The relationship between the phasors of grid voltage, gird current and inverter voltage in the stationary reference frame can be expressed as,

$$\mathbf{U}_{s\alpha\beta} = R\mathbf{I}_{s\alpha\beta} + L\frac{d\mathbf{I}_{s\alpha\beta}}{dt} + \mathbf{V}_{c\alpha\beta} \quad (1)$$

where $\mathbf{U}_{s\alpha\beta}$, $\mathbf{I}_{s\alpha\beta}$ and $\mathbf{V}_{c\alpha\beta}$ are the grid voltage vector, the current vector and rectifier voltage vector respectively. L and R are the filter inductance and resistance respectively. The phasor under stationary reference frame can be expressed as two mutually perpendicular componentsas: $\mathbf{f}_{s\alpha\beta} = f_{s\alpha} + jf_{s\beta}$. Futhermore, the grid voltage can be expressed as the sum of their respective positive and negative sequence components under unbalanced network conditions as,

$$\begin{aligned} \mathbf{U}_{s\alpha\beta}^+ &= u_{s\alpha}^+ + ju_{s\beta}^+, \quad \mathbf{U}_{s\alpha\beta}^- = u_{s\alpha}^- + ju_{s\beta}^- \\ \mathbf{V}_{c\alpha\beta}^+ &= v_{c\alpha}^+ + jv_{c\beta}^+, \quad \mathbf{V}_{c\alpha\beta}^- = v_{c\alpha}^- + jv_{c\beta}^- \\ \mathbf{I}_{s\alpha\beta}^+ &= i_{s\alpha}^+ + ji_{s\beta}^+, \quad \mathbf{I}_{s\alpha\beta}^- = i_{s\alpha}^- + ji_{s\beta}^- \end{aligned} \quad (2)$$

The positive and negative sequence voltage and current components in the stationary reference frame can be expressed as,

$$\begin{aligned} u_{s\alpha}^+ &= |\mathbf{U}_s^+| \cos(\omega_1 t + \phi_u^+), \quad u_{s\alpha}^- = |\mathbf{U}_s^-| \cos(\omega_1 t + \phi_u^-) \\ u_{s\beta}^+ &= |\mathbf{U}_s^+| \sin(\omega_1 t + \phi_u^+), \quad u_{s\beta}^- = -|\mathbf{U}_s^-| \sin(\omega_1 t + \phi_u^-) \\ i_{s\alpha}^+ &= |\mathbf{I}_s^+| \cos(\omega_1 t + \phi_i^+), \quad i_{s\alpha}^- = |\mathbf{I}_s^-| \cos(\omega_1 t + \phi_i^-) \\ i_{s\beta}^+ &= |\mathbf{I}_s^+| \sin(\omega_1 t + \phi_i^+), \quad i_{s\beta}^- = -|\mathbf{I}_s^-| \sin(\omega_1 t + \phi_i^-), \end{aligned} \quad (3)$$

where $|\mathbf{U}_s^+|$, $|\mathbf{U}_s^-|$, $|\mathbf{I}_s^+|$, $|\mathbf{I}_s^-|$ are the amplitudes of positive and negative sequence components of grid voltages and currents respectively, ϕ_u^+ , ϕ_u^- , ϕ_i^+ , ϕ_i^- are the initial phase angle of positive and negative sequence components of grid voltages and currents respectively. Based on the (3), the positive and

negative sequence variation of gird voltage and currents can be expressed as:

$$\begin{aligned} \frac{du_{s\alpha}^+}{dt} &= -\omega_1 u_{s\beta}^+, \quad \frac{du_{s\alpha}^-}{dt} = \omega_1 u_{s\beta}^- \\ \frac{du_{s\beta}^+}{dt} &= \omega_1 u_{s\alpha}^+, \quad \frac{du_{s\beta}^-}{dt} = -\omega_1 u_{s\alpha}^- \\ \frac{di_{s\alpha}^+}{dt} &= -\omega_1 i_{s\beta}^+, \quad \frac{di_{s\alpha}^-}{dt} = \omega_1 i_{s\beta}^- \\ \frac{di_{s\beta}^+}{dt} &= \omega_1 i_{s\alpha}^+, \quad \frac{di_{s\beta}^-}{dt} = -\omega_1 i_{s\alpha}^-, \end{aligned} \quad (4)$$

The active and reactive power can be expressed as,

$$P + jQ = \frac{3}{2} \mathbf{U}_{s\alpha\beta} \times \mathbf{I}_{s\alpha\beta}^* \quad (5)$$

where $\mathbf{I}_{s\alpha\beta}^*$ is the conjugate of grid current. Substituting (2), (3) into (5), the instantaneous active and reactive power under unbalanced network can be calculated as follows,

$$\begin{cases} P = P_{11} + P_{12} + P_{21} + P_{22} \\ Q = Q_{11} + Q_{12} + Q_{21} + Q_{22}, \end{cases} \quad (6)$$

where P_{11} and Q_{11} are the respective components of active power and reactive power generated by positive components' interactions. P_{12} , Q_{12} , P_{21} and Q_{21} are the oscillating components at twice the network frequency generated by positive and negative components' interactions. P_{22} and Q_{22} are the components caused by negative components' interactions. They can be further expressed as follows,

$$\begin{bmatrix} P_{11} \\ P_{12} \\ P_{21} \\ P_{22} \\ Q_{11} \\ Q_{12} \\ Q_{21} \\ Q_{22} \end{bmatrix} = \begin{bmatrix} u_{s\alpha}^+ & 0 & u_{s\beta}^+ & 0 \\ 0 & u_{s\alpha}^+ & 0 & u_{s\beta}^+ \\ u_{s\alpha}^- & 0 & u_{s\beta}^- & 0 \\ 0 & u_{s\alpha}^- & 0 & u_{s\beta}^- \\ u_{s\beta}^+ & 0 & -u_{s\alpha}^+ & 0 \\ 0 & u_{s\beta}^+ & 0 & -u_{s\alpha}^+ \\ u_{s\beta}^- & 0 & -u_{s\alpha}^- & 0 \\ 0 & u_{s\beta}^- & 0 & -u_{s\alpha}^- \end{bmatrix} \begin{bmatrix} i_{s\alpha}^+ \\ i_{s\alpha}^- \\ i_{s\beta}^+ \\ i_{s\beta}^- \end{bmatrix} \quad (7)$$

In conventional DPC method, the active and reactive powers are directly regulated to a constant value, which usually set as reference values while the grid is strictly balanced. However, if the network is unbalanced, following equations should be satisfied for conventional DPC to regulate both active and reactive power at its reference constant value at the same time,

$$\begin{aligned} P_{12} + P_{21} &= 0 \\ Q_{12} + Q_{21} &= 0. \end{aligned} \quad (8)$$

Substituting (3), (7) into (8) yields,

$$|\mathbf{U}_s^+| |\mathbf{I}_s^-| \cos(2\omega_1 t + \phi_d) = -|\mathbf{U}_s^-| |\mathbf{I}_s^+| \cos(2\omega_1 t + \phi_q) \quad (9)$$

$$|\mathbf{U}_s^+| |\mathbf{I}_s^-| \sin(2\omega_1 t + \phi_d) = |\mathbf{U}_s^-| |\mathbf{I}_s^+| \sin(2\omega_1 t + \phi_q), \quad (10)$$

where $\phi_d = \phi_u^+ + \phi_i^-$, $\phi_q = \phi_u^- + \phi_i^+$ Equations (9) and (10) must be satisfied throughout the whole system operation.

According to (9)

$$\begin{aligned} |\mathbf{U}_s^+| |\mathbf{I}_s^-| &= -|\mathbf{U}_s^+| |\mathbf{I}_s^-| \\ \cos(2\omega_1 t + \phi_d) &= \cos(2\omega_1 t + \phi_q). \end{aligned} \quad (11)$$

According to (10),

$$\begin{aligned} |U_s^+||I_s^-| &= |U_s^-||I_s^+| \\ \sin(2\omega_1 t + \phi_d) &= \sin(2\omega_1 t + \phi_q). \end{aligned} \quad (12)$$

It is obvious that (11) and (12) cannot be satisfied simultaneously, in other words, to completely restrain the active power and reactive ripples at the same time is impossible. A widely adopted solution to overcome this problem is to adding the compensation terms at both active and reactive power references to meet different control targets. There are mainly three targets as follows, (1) to obtain sinusoidal and symmetrical currents; (2) to remove reactive power ripple; (3) to cancel active power ripple. The control strategy proposed in this paper is mainly used to meet target(1). As can be seen from (7), P_{22} and Q_{22} are generated by the interaction of voltage and currents negative sequences, which means the negative sequence components of currents can be eliminated if those two components are regulated to zero. The following section will further discuss how the proposed D-GVM-DPC strategy can achieve this object.

III. DESIGN OF PROPOSED D-GVM-DPC

The main idea of this method is based on the structure of GVM-DPC proposed in [4]. In order to guarantee that two sequences can be well regulated, it is necessary to independently control each sequence. The original controller is then substituted by two controllers: one for the positive-sequence powers and one for negative-sequence powers. The positive sequence controller is used generate a proper positive converter voltage vector to regulate the output active power and reactive power at its reference value, while the negative sequence controller is used to generate negative converter voltage vector to restrain the ripple of currents. By differentiating (7), the instantaneous active and reactive powers P_{11} and Q_{11} can be expressed as,

$$\begin{aligned} \frac{dP_{11}}{dt} &= \frac{3}{2} \left(\frac{du_{s\alpha}^+}{dt} i_{s\alpha}^+ + \frac{du_{s\beta}^+}{dt} i_{s\beta}^+ + \frac{di_{s\alpha}^+}{dt} u_{s\alpha}^+ + \frac{di_{s\beta}^+}{dt} u_{s\beta}^+ \right) \\ \frac{dQ_{11}}{dt} &= \frac{3}{2} \left(\frac{du_{s\beta}^+}{dt} i_{s\alpha}^+ - \frac{du_{s\alpha}^+}{dt} i_{s\beta}^+ + \frac{di_{s\alpha}^+}{dt} u_{s\beta}^+ - \frac{di_{s\beta}^+}{dt} u_{s\alpha}^+ \right). \end{aligned} \quad (13)$$

Substituting (1), (2) and (4) into (13), the dynamics of the instantaneous active and reactive powers P_{11} and Q_{11} can be obtained as follows,

$$\begin{aligned} \frac{dP_{11}}{dt} &= -\omega_1 Q_{11} - \frac{R}{L} P_{11} + \frac{3}{2L} [|U_s^+|^2 - (v_{c\alpha}^+ u_{s\alpha}^+ + v_{c\beta}^+ u_{s\beta}^+)] \\ \frac{dQ_{11}}{dt} &= \omega_1 P_{11} - \frac{R}{L} Q_{11} - \frac{3}{2L} (v_{c\alpha}^+ u_{s\beta}^+ - v_{c\beta}^+ u_{s\alpha}^+). \end{aligned} \quad (14)$$

Define U_P^+ and U_Q^+ as the Voltage Modulated Regulation (VMR) inputs which can be expressed as,

$$\begin{aligned} U_P^+ &= v_{c\alpha}^+ u_{s\alpha}^+ + v_{c\beta}^+ u_{s\beta}^+ \\ U_Q^+ &= v_{c\alpha}^+ u_{s\beta}^+ - v_{c\beta}^+ u_{s\alpha}^+. \end{aligned} \quad (15)$$

By manipulating (14) and (15), the VMR inputs can be also expressed as,

$$\begin{aligned} U_P^+ &= \nu_P - \frac{2L}{3} \omega_1 Q_{11} + |U_s^+|^2 \\ U_Q^+ &= \nu_Q + \frac{2L}{3} \omega_1 P_{11}, \end{aligned} \quad (16)$$

where ν_P and ν_Q are the new control inputs. The power regulation system can be transformed into a LTI system as,

$$\begin{aligned} \nu_P^+ &= \frac{2L}{3} \left(\frac{dP_{11}}{dt} + \frac{R}{L} P_{11} \right) \\ \nu_Q^+ &= \frac{2L}{3} \left(\frac{dQ_{11}}{dt} + \frac{R}{L} Q_{11} \right). \end{aligned} \quad (17)$$

There are various control methods could be designed to generate ν_P and ν_Q , for simplicity and reliability, the PI controllers $F_P(s)$ for both components are designed as,

$$\begin{aligned} \nu_P^+ &= K_{p,P} (P_{11}^* - P_{11}) + K_{i,P} \int (P_{11}^* - P_{11}) dt \\ \nu_Q^+ &= K_{p,Q} (Q_{11}^* - Q_{11}) + K_{i,Q} \int (Q_{11}^* - Q_{11}) dt. \end{aligned} \quad (18)$$

The method about how to properly select proportional and integral gains to determining the system's natural frequency and damping factor is discussed in [2].

Finally, using the inversion of (15), voltage vector reference value of the positive sequence controller can be calculated as follows,

$$\begin{cases} v_{c\alpha}^+ = \frac{u_{s\alpha}^+ U_P^+ + u_{s\beta}^+ U_Q^+}{|U_s^+|^2} \\ v_{c\beta}^+ = \frac{u_{s\beta}^+ U_P^+ - u_{s\alpha}^+ U_Q^+}{|U_s^+|^2}. \end{cases} \quad (19)$$

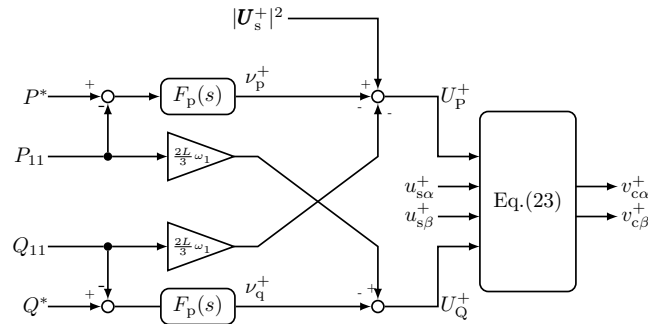


Fig. 1. Proposed UVM-DPC for positive sequence regulation.

As for the design of negative sequence controller, by differentiating (7) the instantaneous active and reactive power P_{22} and Q_{22} can be expressed as,

$$\begin{aligned} \frac{dP_{22}}{dt} &= \frac{3}{2} \left(\frac{du_{s\alpha}^-}{dt} i_{s\alpha}^- + \frac{du_{s\beta}^-}{dt} i_{s\beta}^- + \frac{di_{s\alpha}^-}{dt} u_{s\alpha}^- + \frac{di_{s\beta}^-}{dt} u_{s\beta}^- \right) \\ \frac{dQ_{22}}{dt} &= \frac{3}{2} \left(\frac{du_{s\beta}^-}{dt} i_{s\alpha}^- - \frac{du_{s\alpha}^-}{dt} i_{s\beta}^- + \frac{di_{s\alpha}^-}{dt} u_{s\beta}^- - \frac{di_{s\beta}^-}{dt} u_{s\alpha}^- \right). \end{aligned} \quad (20)$$

Substituting (1), (2) and (4) into (20), the dynamics of the instantaneous active and reactive powers P_{11} and Q_{11} can be obtained as follows,

$$\begin{aligned} \frac{dP_{22}}{dt} &= \omega_1 Q_{22} - \frac{R}{L} P_{22} + \frac{3}{2L} [|U_s^-|^2 - (v_{c\alpha}^- u_{s\alpha}^- + v_{c\beta}^- u_{s\beta}^-)] \\ \frac{dQ_{22}}{dt} &= \omega_1 P_{22} - \frac{R}{L} Q_{22} - \frac{3}{2L} (v_{c\alpha}^- u_{s\beta}^- - v_{c\beta}^- u_{s\alpha}^-). \end{aligned} \quad (21)$$

Define U_P^- and U_Q^- as the VMR inputs for negative sequence controller which can be expressed as,

$$\begin{aligned} U_P^- &= v_{c\alpha}^- u_{s\alpha}^- + v_{c\beta}^- u_{s\beta}^- \\ U_Q^- &= v_{c\alpha}^- u_{s\beta}^- - v_{c\beta}^- u_{s\alpha}^-. \end{aligned} \quad (22)$$

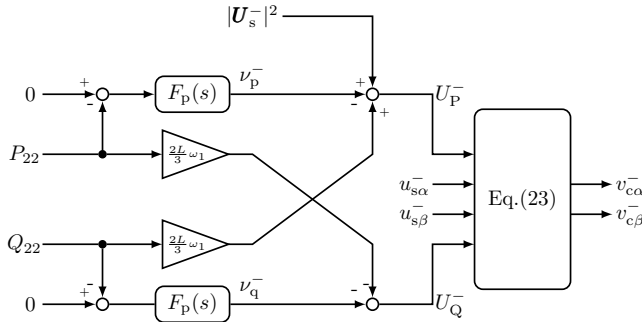


Fig. 2. Proposed UVM-DPC for negative sequence regulation.

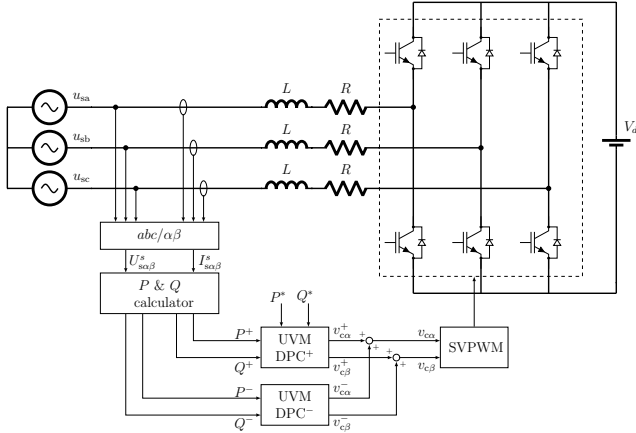


Fig. 3. Scheme diagram of improved GVM-DPC based dual control strategy for unbalanced network condition.

By manipulating (21) and (22), the VMR inputs can be also expressed as,

$$\begin{aligned} U_P^- &= \nu_P + \frac{2L}{3} \omega_1 Q_{22} + |U_s^-|^2 \\ U_Q^- &= \nu_Q - \frac{2L}{3} \omega_1 P_{22}, \end{aligned} \quad (23)$$

TABLE I
PARAMETERS OF THE SIMULATED SYSTEM

Rated power	2 MW
Line-to line voltage (rms)	690 V
frequency f	50 Hz
R (ohm)	0.00002
Ls (mH)	0.4
$K_{p,P}$	0.3
$K_{i,P}$	5
$K_{p,Q}$	0.3
$K_{i,Q}$	5

where ν_P and ν_Q are the new control inputs. The power regulation system can be transformed into a LTI system as,

$$\begin{aligned} \nu_P^- &= \frac{2L}{3} \left(\frac{dP_{22}}{dt} + \frac{R}{L} P_{22} \right) \\ \nu_Q^- &= \frac{2L}{3} \left(\frac{dQ_{22}}{dt} + \frac{R}{L} Q_{22} \right). \end{aligned} \quad (24)$$

In this paper, the parameters of negative sequence controller are chosen to be the same to positive sequence controller as $K_{p,P}$ and $K_{i,P}$ respectively. PI controllers $F_p(s)$ for both components are designed as,

$$\begin{aligned} \nu_P^- &= K_{p,P} (P_{22}^* - P_{22}) + K_{i,P} \int (P_{22}^* - P_{22}) dt \\ \nu_Q^- &= K_{p,Q} (Q_{22}^* - Q_{22}) + K_{i,Q} \int (Q_{22}^* - Q_{22}) dt. \end{aligned} \quad (25)$$

Using the inversion of (22), voltage vector reference value of the negative sequence controller can be calculated as follows,

$$\begin{cases} v_{c\alpha}^- = \frac{u_{s\alpha}^- U_P^- + u_{s\beta}^- U_Q^-}{|U_s^-|^2} \\ v_{c\beta}^- = \frac{u_{s\beta}^- U_P^- - u_{s\alpha}^- U_Q^-}{|U_s^-|^2}. \end{cases} \quad (26)$$

The positive sequence controller and negative sequence controller are shown in Fig.1 and Fig.2 respectively. It should be noted that the plus-minus sign of power compensation terms are different between two controllers.

IV. SIMULATION RESULTS

Simulation results of the proposed D-GVM-DPC strategy for SVPWM converter is carried out by using MATLAB/Simulink. The scheme of the implemented model is shown in Fig.3. The requirements for converters are designed as follows, obtaining the sinusoidal currents; unity power factor; low ripple of active & reactive powers. The parameters of simulated system are proposed in Table I. The power reference are set as $P_{ref} = 1MW$ and $Q_{ref} = 0Mvar$. To verify the effectiveness of proposed strategy, four different controllers are built and compared in this section. Controllers to be compared including classic VOC based PI controller, general VOC based dual controller, GVM-DPC proposed in [4] and proposed D-GVM-DPC based dual control strategy. The simulation time step is set to $5 \mu s$ and the sampling frequency is set to 10 kHz.

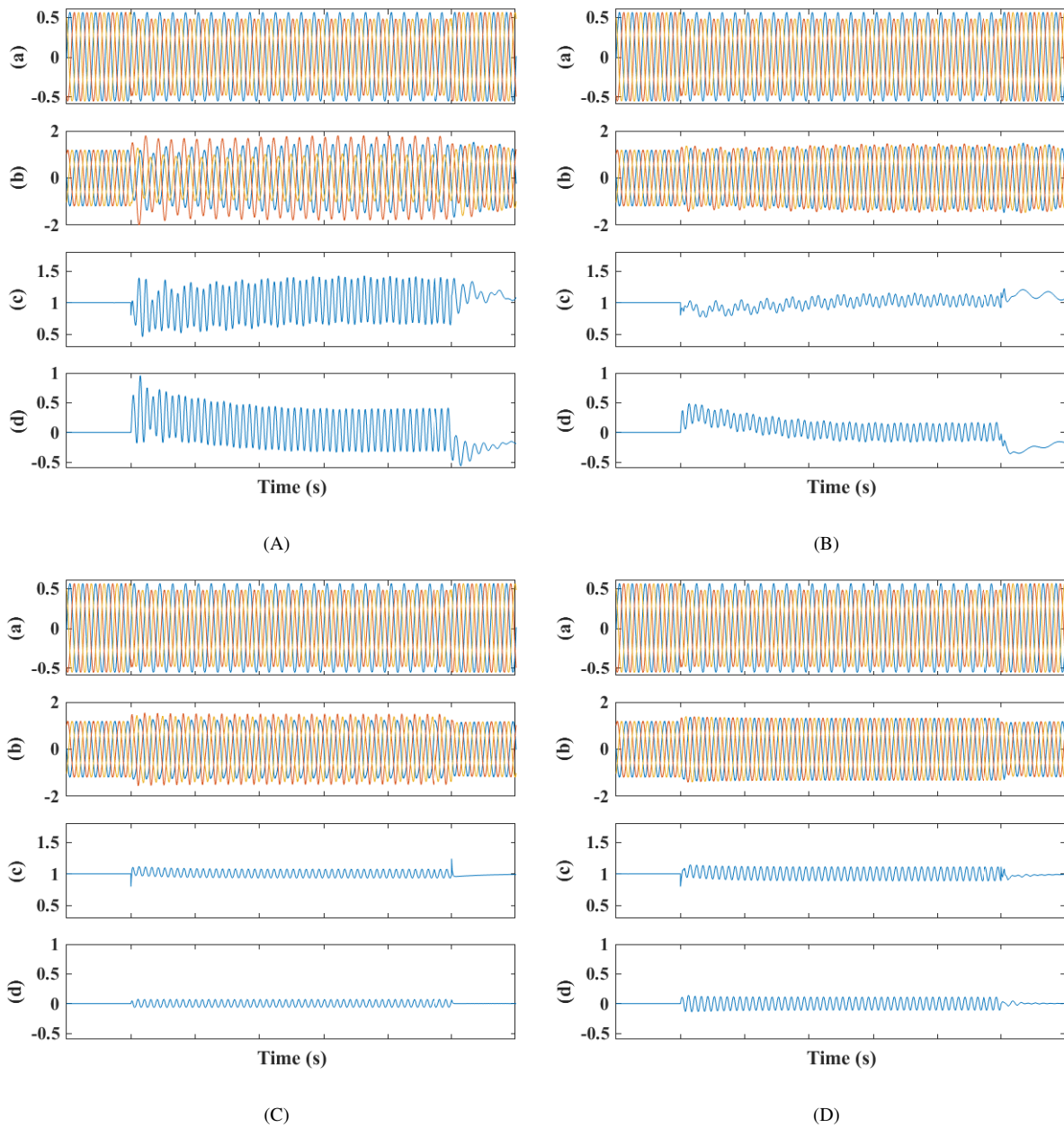


Fig. 4. Responses of currents and active power & reactive power under 20% grid voltage dips in phase A and phase B. (a) Three-phase voltages(kV). (b) Three-phase currents (kA). (c) Active power (MW). (e) Reactive power (MVar). (A) Classic VOC based PI controller. (B) General dual-control strategy for VOC. (C) GVM-DPC controller proposed in [4]. (D) Proposed D-GVM-DPC strategy

Dynamic responses of currents, active and reactive powers between four controllers under two phase network voltage dips at 20% are presented as shown in Fig. 4. The ripples of active and reactive powers of DPC based controllers shown in (C) and (D) are much lower than that of general VOC based controllers (A) and (B). Comparing (A) and (B) in Fig.4, it can be concluded that general dual control strategy for VOC can effectively restrain the active and reactive power ripples. as shown in Fig.4 (C), the original proposed GVM-DPC already has a pretty satisfied power dynamics. The proposed D-GVM-DPC however, make the power dynamics a slight change

for the worse. But the dynamics are still better than VOC strategies.

Current spectra of phase A are shown in Fig.5 (A)-(D) with different control strategies under 20 % network voltage dips in phase A and phase B. The dual control strategy for VOC can effectively improve the power ripples and unbalances of currents, but there is almost no distinction of their Total Harmonic Distortion (THD). The THD of original GVM-DPC under unbalanced grid voltage is 11.31%, which is higher than commonly required value (5%) for grid operation. On the contrary, the current THD of the converter using proposed

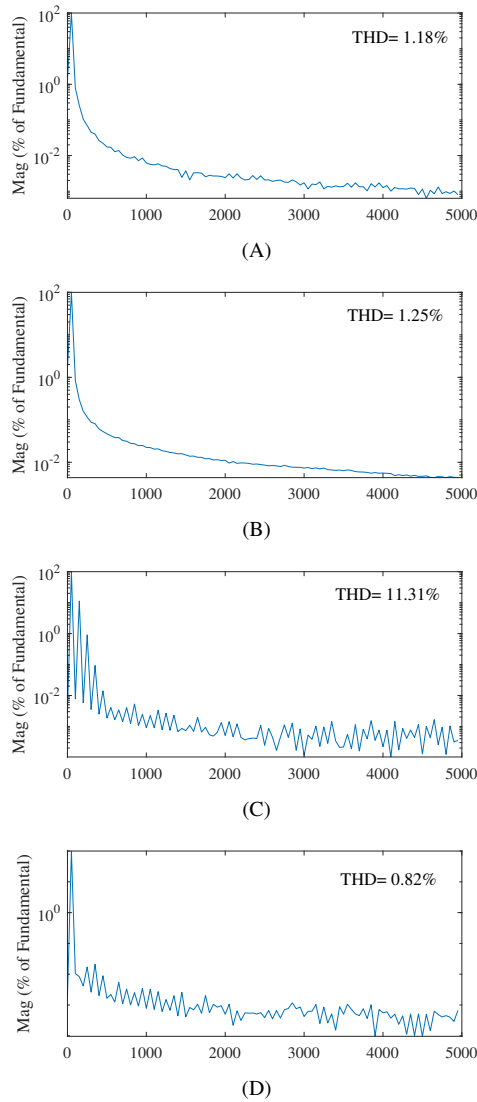


Fig. 5. Current harmonic spectra under 20% unbalanced voltage condition. (A) Classic VOC based PI controller. (B) General dual-control strategy for VOC. (C) GVM-DPC controller proposed in [4]. (D) Proposed D-GVM-DPC strategy

D-GVM-DPC is much lower than that of original GVM-DPC and grid requirement, the performance is even better than VOC based controllers. Thus, the proposed method is verified.

V. CONCLUSION

Currently, the existing methods for DPC under unbalanced network conditions are mainly modifying the original power references by adding compensations. In this paper, a D-GVM-DPC control strategy for grid-connected VSC under unbalanced network is proposed. The concept of D-GVM-DPC is by separately regulating the positive power sequence and negative power sequence to restrain current ripples under voltage distortions. The mathematical model of VSC under unbalanced network condition is fully discussed and the principles and design of proposed method is explained in detail. The

effectiveness of D-GVM-DPC is verified by the simulation results.

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